

Nexus 5001™ Forum Introduces New Member Companies and Provides Details of the Updated Nexus 5001 Embedded-Debug Interface Standard

Visteon and dSPACE join as New Members; Member Companies developing New Nexus-Conforming Microprocessors

Embedded Systems Conference, San Francisco, 23 April 2003 – The Nexus 5001™ Forum announced today a major step toward establishing a de facto global standard for Embedded Debugging with the introduction of the two newest members to the Forum, dSPACE and Visteon Technologies. The Forum also announced details of the updated IEEE-ISTO Nexus 5001 Standard. The revised Nexus Standard is in the final stages of review and is scheduled for a vote by the Nexus Forum by the end of April. Ratification of the new standard will enable member companies, as well as the industry as a whole, to work with an enhanced version that will provide new Data and Instruction trace capabilities and a recommended connector.

The recent inclusion of dSPACE and Visteon Technologies brings the Nexus Forum membership to 18 companies. These two companies have specifically joined the Nexus consortium to provide their customers with the flexibility and common interface that the Standard provides.

dSPACE comes to the Nexus Forum with a strong knowledge base in rapid-prototyping tools specialized for the rugged environment found in automotive applications. Visteon provides the Nexus Forum with a wealth of systems knowledge in automotive electronics as an end user of the tools that Nexus is defining.

"dSPACE is very pleased to be a new member of the Nexus 5001 Forum. The Nexus standard addresses many of the challenges we face as processor bus speeds increase and conventional external memory interfaces become less practical or even impossible in some applications. We plan to take full advantage of the new provisions in the updated Nexus standard in our upcoming product offerings" said Kevin Kott, President of dSPACE's North American Headquarters. The dSPACE product line includes systems for Rapid Controller Prototyping, Rapid Algorithm Development, Production Code Generation, Hardware-In-The-Loop Simulation and Calibration.

"We're very pleased to welcome dSPACE and Visteon to the Nexus 5001 Forum," said Peter Lefkin, COO of the IEEE-ISTO, which manages the Forum. "The addition of these quality organizations broadens the scope of knowledge for the Forum. We also fully support the updated release to the Nexus Standard as an enhancement to an already successful initiative."

The Forum is committed to providing the microprocessor industry with the first standardized method of connecting development tools to Nexus compliant microprocessors, DSPs and MCUs spanning multiple architectures. The method of transferring information between the processor and the development tool has been standardized to include the formatting of the data into compressed packets. The specification includes the common development interface functions on the processors, such as breakpoint and watchpoint registers.

"The Nexus 5001 Forum's Technical Committee has been dedicated to providing more details to the existing standard, such as support for increased clock speeds and multi-processor devices. The

